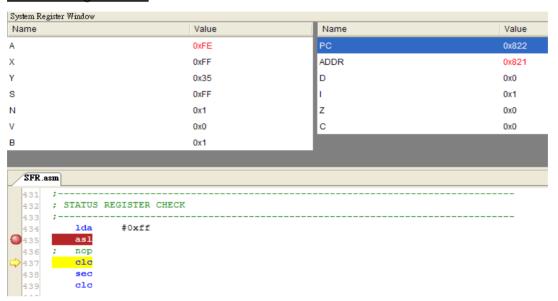


## **Application Note of Using NY8L ICE**

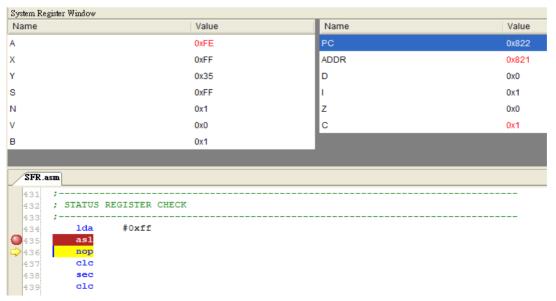
In single-step debugging using NY8L ICE, step through the instructions which will affect the C flag / V flag, and if the next instruction is CLC/SEC/CLV, the status of C flag / V flag displayed on NYIDE may be inconsistent with the execution result of that instruction. This is because when executing Single Step, NY8L ICE will prefetch the next instruction, and induces the change of Status Register. However, the final outcome will not be affected.

Ex 1: After line 435 is executed, C flag is supposed to be 1. However, due to clc instruction is followed right after, the C flag on NYIDE is 0. Status Register has been changed although the clc instruction has not been executed yet. But the ultimate outcome will still be the same.

## NYIDE debug window:



If a nop instruction is inserted at line 436, the execution result of line 435 will be as expected. There is no need for user to worry about the execution of program.



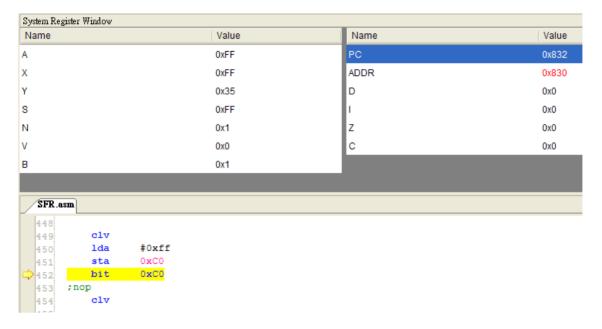
1

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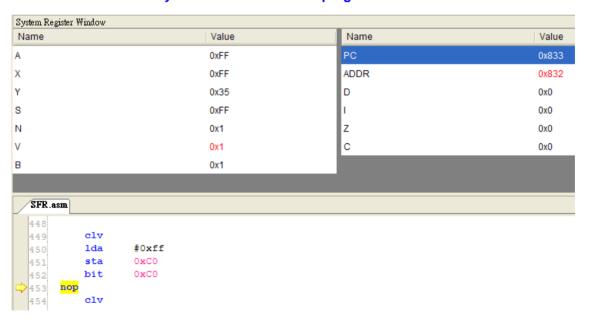


Ex 2: After line 452 is executed, V flag is supposed to be 1. However, due to clv instruction is followed right after, the V flag on NYIDE is 0. Status Register has been changed although the clv instruction has not been executed yet. But the ultimate outcome will still be the same.

## NYIDE debug window:



If a nop instruction is inserted at line 453, the execution result of line 452 will be as expected. There is no need for user to worry about the execution of program.



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